

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,089,367 B1  
APPLICATION NO. : 09/372296  
DATED : August 8, 2006  
INVENTOR(S) : Koker et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

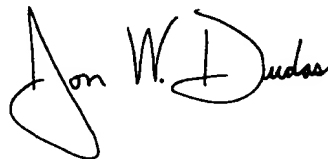
In column 6, at line 40, delete "CYCLE" and insert --CYCLE1--.

In column 9, at line 28, delete "9" and insert --8--.

Delete claim 8, and insert the following claim --An apparatus comprising: a pre-fetcher to pre-fetch a plurality of data from a memory to a data queue in response to a request; a queue controller coupled to the data queue and the pre-fetcher to deliver the pre-fetched data from the data queue to a bus independently of the memory; a data coherence controller coupled to the pre-fetcher to provide a purge signal when the request corresponds to a cache miss; and a scheduler coupled to the data coherence controller to store entries corresponding to the request, the entries being marked according to the purge signal from the data coherence controller, the marked entries corresponding to data to be purged.--.

Signed and Sealed this

Tenth Day of June, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*